

ABSTRACT

In an embodiment of a matrix switch circuit, a group provided with twelve 64-to-1 selectors is arranged by 64 pieces, that is, (12 x 64) pieces of 64-to-1 selectors are provided.

- 5 Address information is input to the selector. STS-12 (one frame) in which twelve 8-bit STS-1 (unit data) are serially arrayed is input from 64 pieces of input terminals of the selector. The matrix switch circuit is further provided with a selecting circuit that selects specific unit data in the frame and 64 pieces
- 10 of 12-to-1 selectors that forms unit data output from the selecting circuit in one frame and outputs it.

Patented by the U.S. Patent and Trademark Office